

EV026160662US

#2/2002
3/8/02
J1017 U.S. PTO
10/051981
01/16/02

Inventor: Zhongze Wang

Title: Silicon-On-Insulator Comprising Integrated Circuitry and Methods of Forming Silicon-On-Insulator Circuitry

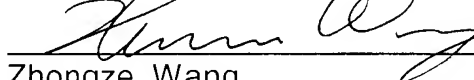
Assignee: Micron Technology, Inc.

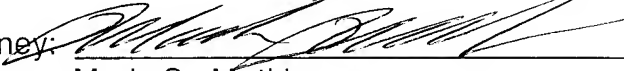
INFORMATION DISCLOSURE STATEMENT

References – See Attached Form PTO-1449

The attached form PTO-1449 is submitted in compliance with 37 CFR § 1.56. Copies of the cited art are included. No admission is made regarding whether all the submitted references are prior art.

Respectfully submitted,

Dated: 1/2/2002 Inventor: 
Zhongze Wang

Dated: 1-16-02 Attorney: 
Mark S. Matkin
Reg. No. 32,268

Form PTO-1449		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY. DOCKET NO. M122-1797	SERIAL NO. Unknown		
LIST OF ART CITED BY APPLICANT (Use several sheets if necessary)				APPLICANT Micron Technology, Inc.			
				FILING DATE Filed Herewith		GROUP Unknown	
U.S. PATENT DOCUMENTS							
*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
	AA						
	AB						
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						
	AK						
FOREIGN PATENT DOCUMENTS							
		Document Number	Date	Country	Class	Subclass	Translation
							Yes No
	AL						
	AM						
	AN						
	AO						
	AP						
OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, Etc.)							
	AE		Floating Body Effects: SOI DEVICE ELECTRICAL PROPERTIES; pp. 34-53 (pre Aug. 2001)				
	AS						
	AT						
EXAMINER				DATE CONSIDERED			
*EXAMINER Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.							

 J1017 U.S. PTO
 10/05/01
